

July 12, 2005



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Craig L. Stevens et al.

Serial No. 09/872,796

Examiner: Erik J. Kielin

Filing Date: 06/01/2001

Art Unit: 2813

Title: Architecture For High Throughput Semiconductor Processing Applications

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

INTRODUCTORY COMMENTS

Sir:

This paper is responsive to the office action mailed on May 19, 2005 (hereinafter the "last office action").

Copy to order, CLK: 7/29/2005

AT/